Please enter the following amendments:

<u>In the Specification:</u>

Please amend the Cross Reference paragraph on page 1, lines 4-9 as follows:

Cross Reference To Related Application

This application is related to copending U.S. Patent Application Serial No. 09/888, 278, now U.S. Patent 6,775,727 entitled "System and Method For Controlling Bus Arbitration During Cache Memory Burst Cycles" and assigned to the assignee hereof.